## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re A	Application of:	)	
	Kenneth S. McElvain, et al.	Examiner:	Not yet assigned
Application No		Art Unit:	Not yet assigned
Filed:	December 8, 2003	<b>)</b>	
For:	METHODS AND APPARATUSES FOR DESIGNING INTEGRATED CIRCUITS	<b>)</b>	
P.O. E	nissioner for Patents Box 1450 ndria, Virginia 22313-1450		

## INFORMATION DISCLOSURE STATEMENT

Sir:

Applicant hereby requests consideration of the enclosed Information Disclosure Statement pursuant to 37 C.F.R. §1.97(b). Attached are the PTO Forms 1449 and Forms 892 from the parent application filed on May 17, 1999 (Serial No. 09/310,882) and from the continuation application filed on June 3, 2002 (Serial No. 10/162,270). These previous applications are relied upon for an earlier filing date under 35 U.S.C. §120.

Pursuant to C.F.R. §1.98(d), copies of the references are not being provided herewith since they were previously sent to the Patent and Trademark Office. Please consider these cited documents in the currently pending §1.53(b) continuation application filed on December 8, 2003.

## EXPRESS MAIL CERTIFICATE OF MAILING

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Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 12/8, 2003

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